PRELIMINARY

Notice: This is not a final specification Some parametric are subject to change.

RTBN131AP1

TRANSISTOR WITH RESISTOR FOR SWITHING APPLICATION SILICON NPN EPITAXIAL TYPE

DESCRIPTION

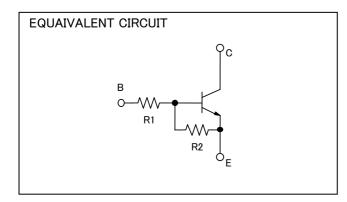
RTBN131AP1 is a one chip transistor with built-in bias transistor.

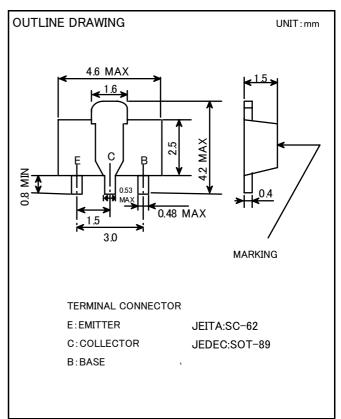
FEATURE

- •Built-in bias resistor (R1=1k Ω ,R2=1k Ω)
- •High collector current (IC=1A)
- ·Small package for easy mounting.

APPLICATION

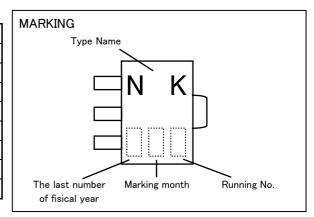
Switching.





MAXIMUM RATING (Ta=25°C)

SYMBOL	PARAMETER	RATING	UNIT
V _{CBO}	Collector to Base voltage	80	٧
V_{EBO}	Emitter to Base voltage	10	٧
V _{CEO}	Collector to Emitter voltage	60	٧
I c	Collector current	1	Α
I _{CM}	Peak collector current	2	Α
P _{CM}	Collector dissipation(Ta=25°C)	500	mW
T _j	Junction temperature	+150	°C
T_{stg}	Storage temperature	-55 ~ +150	°C



ELECTRICAL CHARACTERISTICS (Ta=25°C)

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			LINIT
			MIN	TYP	MAX	UNIT
I_{CBO}	Collector cut off current	V_{CE} =60V, I_{E} =0	_	_	0.1	μΑ
VI(on)	Input on voltage	V _{CE} =0.35V, I _C =0.4A	_	_	5.0	٧
VI(off)	Input off voltage	V_{CE} =5V, I_{C} =100 μ A	0.3	_	_	٧
hFE1	DC forward current gain1	V _{CE} =2V, I _C =0.1A	80	_	_	_
hFE2	DC forward current gain2	V _{CE} =2V, I _C =0.5A	200	_	_	_
hFE3	DC forward current gain3	V _{CE} =2V, I _C =1A	200	_	_	_
R1	Input resistor	_	0.7	1.0	1.3	kΩ
R2	Emitter-base resistor	_	0.7	1.0	1.3	kΩ



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